

OCT 26 2005



Fitch, Even, Tabin & Flannery

Intellectual Property Law

F A C S I M I L E

Name: U.S. Patent & Trademark Office
FAX: (571) 273-8300
Application No.: 10/728,036
Filing Date: December 3, 2003
Inventor: Cam L. Lu
Art Unit: 2825
Examiner: SIEK, VUTHE
From: Eric J. Whitesell, Reg. No. 38,657 *EW*
Today's Date: October 26, 2005
Attorney Docket No.: 03-1978 81641
Pages: 38 pages (including this 1-page coversheet)
Dispatched by: Julie Freiburger

Transmitted herewith for filing via facsimile:

Transmittal (1 pg.);
Response to Office Action Under 37 C.F.R. § 1.111 (4 pgs.);
Declaration Under 37 C.F.R. § 1.131 (3 pgs.);
Invention Disclosure (3 pgs.);
"Transition Delay Faults and Stuck-At Faults Test Patterns Generation Flow" (20 pgs.);
LSI IP Dept. Memo dated 10/2/03 (2 pgs.);
Letter dated 10/20/03 (2 pgs.); and
Letter dated 11/24/03 (2 pgs.)

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PTO/SB/21 (09-04)

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TRANSMITTAL
FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission


38

Application Number	10/728,036
Filing Date	December 3, 2003
First Named Inventor	Cam L. Lu
Art Unit	2825
Examiner Name	SIEK, VUTHE
Attorney Docket Number	03-1978 81641

ENCLOSURES (Check all that apply)


<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input checked="" type="checkbox"/> Amendment/Reply (4 pgs.) <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Reply to Missing Parts/ Incomplete Application <input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____ <input type="checkbox"/> Landscape Table on CD	<input type="checkbox"/> After Allowance Communication to TC <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): 1) Declaration Under 37 CFR 1.131; 2) Invention Disclosure; 3) Paper filed "Transition Delay Faults..."; 4) LSI IP Dept. Memo dated 10/2/03; 5) Letter dated 10/20/03; 6) Letter dated 11/24/03; and 7) Facsimile Cover Sheet
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Fitch, Even, Tabin & Flannery		
Signature			
Printed name	Eric J. Whylesell		
Date	October 26, 2005	Reg. No.	38,657

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Signature			
Typed or printed name	Julie Freiburger	Date	October 26, 2005

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Amendment "A" page 1 of 4
10/728,036

DOCKET NO. 03-1978
81641(6653)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): LU, Cam L.
et al.

Serial No.: 10/728,036

Filed: December 3, 2003

For: METHOD OF GENERATING AN
EFFICIENT STUCK-AT FAULT
AND TRANSITION DELAY
FAULT TRUNCATED SCAN TEST
PATTERN FOR AN INTEGRATED
CIRCUIT DESIGN

Art Unit: 2825

Examiner: Siek, Vuthe

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October 26, 2005

Julia Freiburger

RESPONSE TO OFFICE ACTION UNDER 37 C.F.R. § 1.111

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Alexandria, VA 22313-1450

Dear Sir:

Please enter the following response to the Office
Action mailed on August 16, 2005: